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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Boerstler**

Serial No.: **09/666,277**

Filed: **September 21, 2000**

For: **Method and System for  
Clock/Data Recovery for Self-  
Clocked High Speed Interconnects**

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Group Art Unit: **2634**

Examiner: **Pathak, Sudhanshu C.**

Attorney Docket No.: **AUS9-2000-0240-US1**

**LETTER TO THE OFFICIAL DRAFTSMAN**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are formal drawings (11 sheets) for the above-identified application.

Respectfully submitted,

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